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EFS ID:

13172

Application ID:

09683243

Title of Invention:

Integrated level shift and override

circuit for a processor system

First Named Inventor:

Chia-Hsing Yu

Domestic/Foreign Application:

Domestic Application

Filing Date:

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Effective Receipt Date:

2001-12-05

Submission Type:

Utility Patent Filing

Filing Type:

new-utility

Confirmation Number:

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VIAPO010USA

Digital Certificate Holder:

cn=Winston Hsu, ou=Registered Attorneys, ou=Patent and Trademark Office, ou=Department of Commerce, o=U.S.

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Total Fees Authorized:

\$780.0

Payment Category:

DA - Deposit Account

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500801

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WINSTON HSU





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Stylesheet Version: 1.0

Attorney Docket Number: VIAP0010USA

Integrated level shift and override circuit for a processor system

First Named Inventor: Chia-Hsing Yu

SUBMITTED BY

Name:

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41,526

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Date Signed: 20011205

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BASIC FILING FEE

Fee Description	Fee Description Fee Code	
Utility Filing Fee	101	\$ 740

Subtotal For Basic Filing Fee: \$ 740

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 15	103	\$ 18	0	\$ O
Independent Claims: 1	102	\$ 84	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0

ADDITIONAL FEES

Fee Description	Fee Code	Fee Paid
Recording Each Patent Assignment Per Property Fee	581	\$ 40

Subtotal For Additional Fees: \$ 40